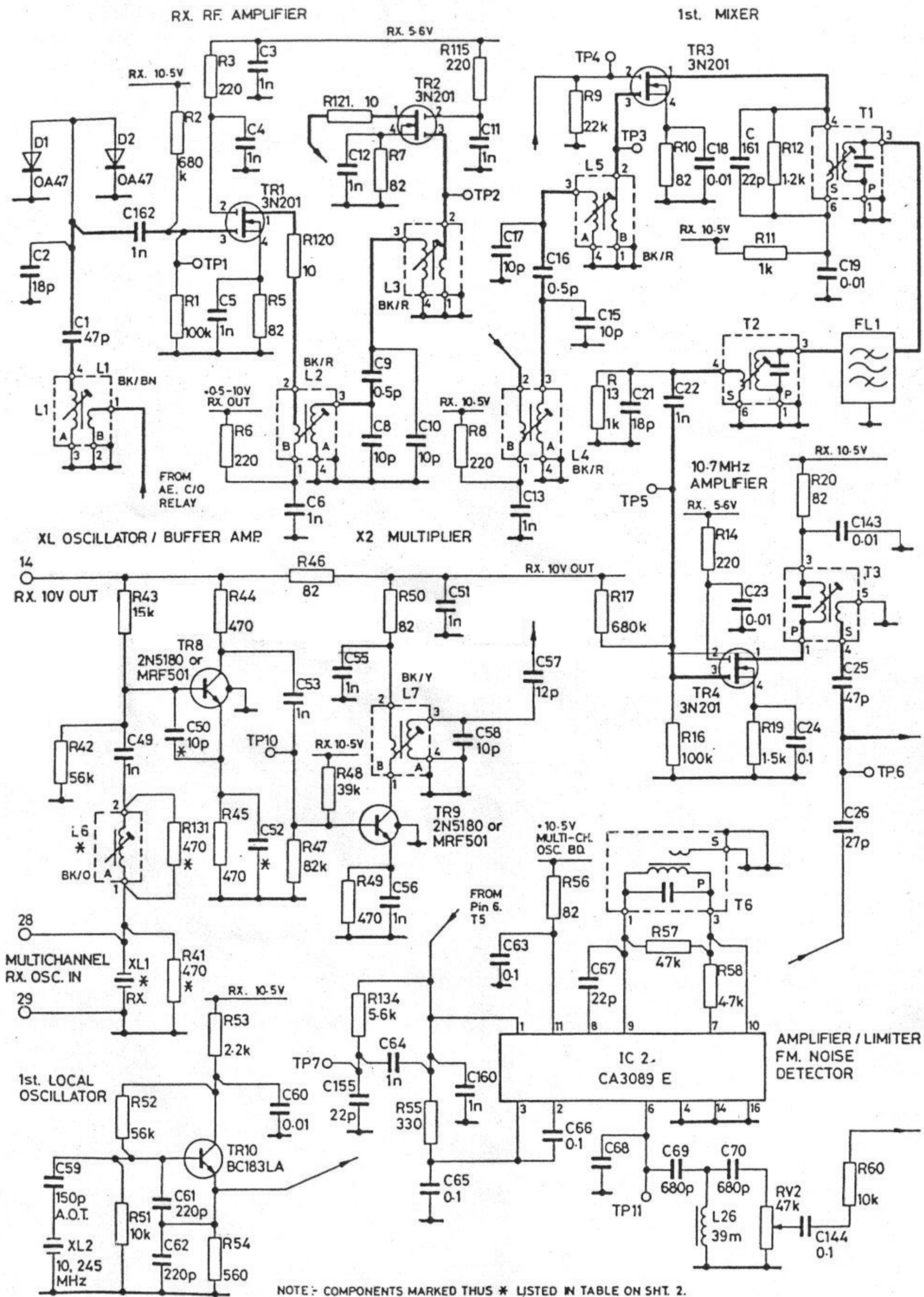
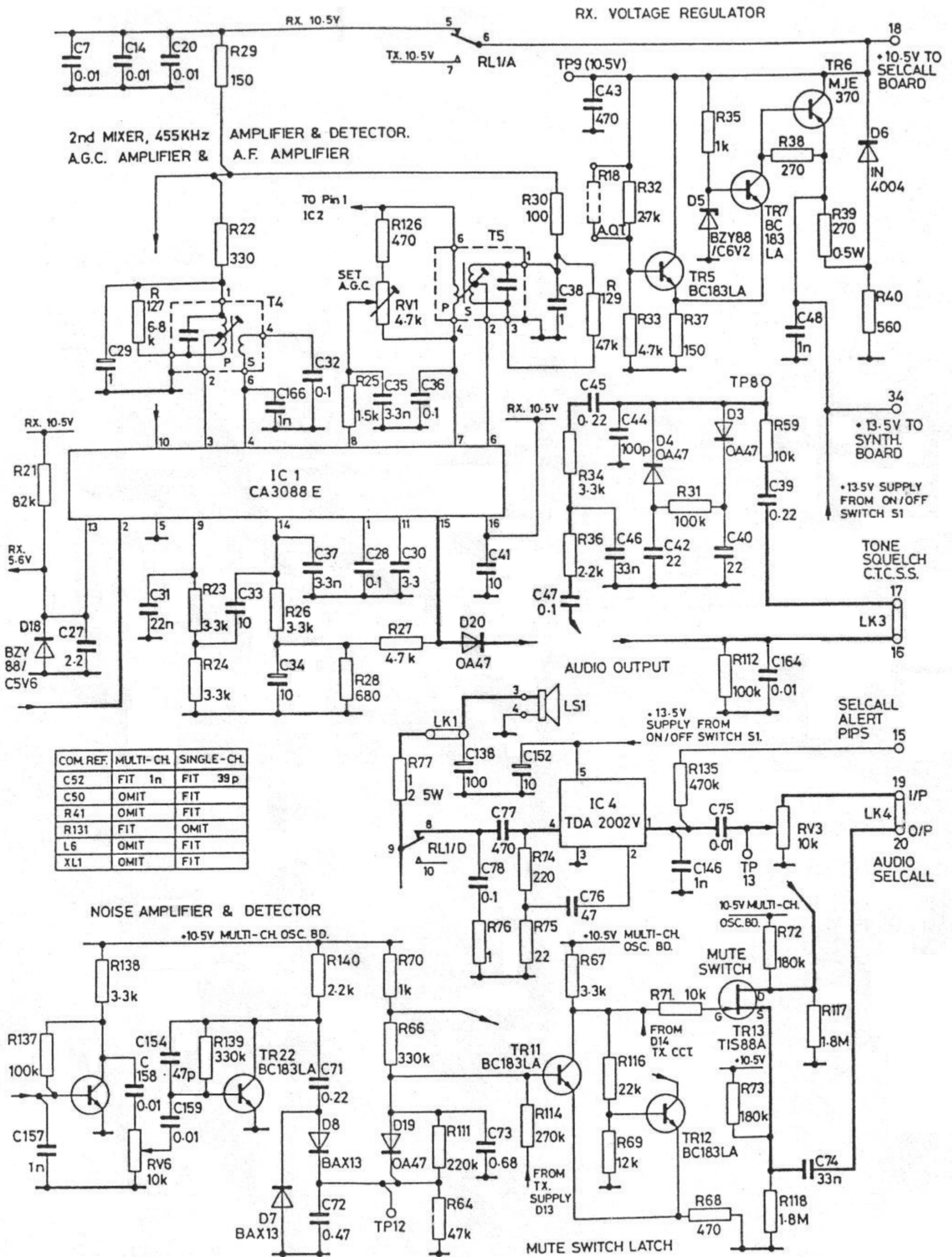


TRANSMITTER CIRCUIT DIAGRAM Fig 17

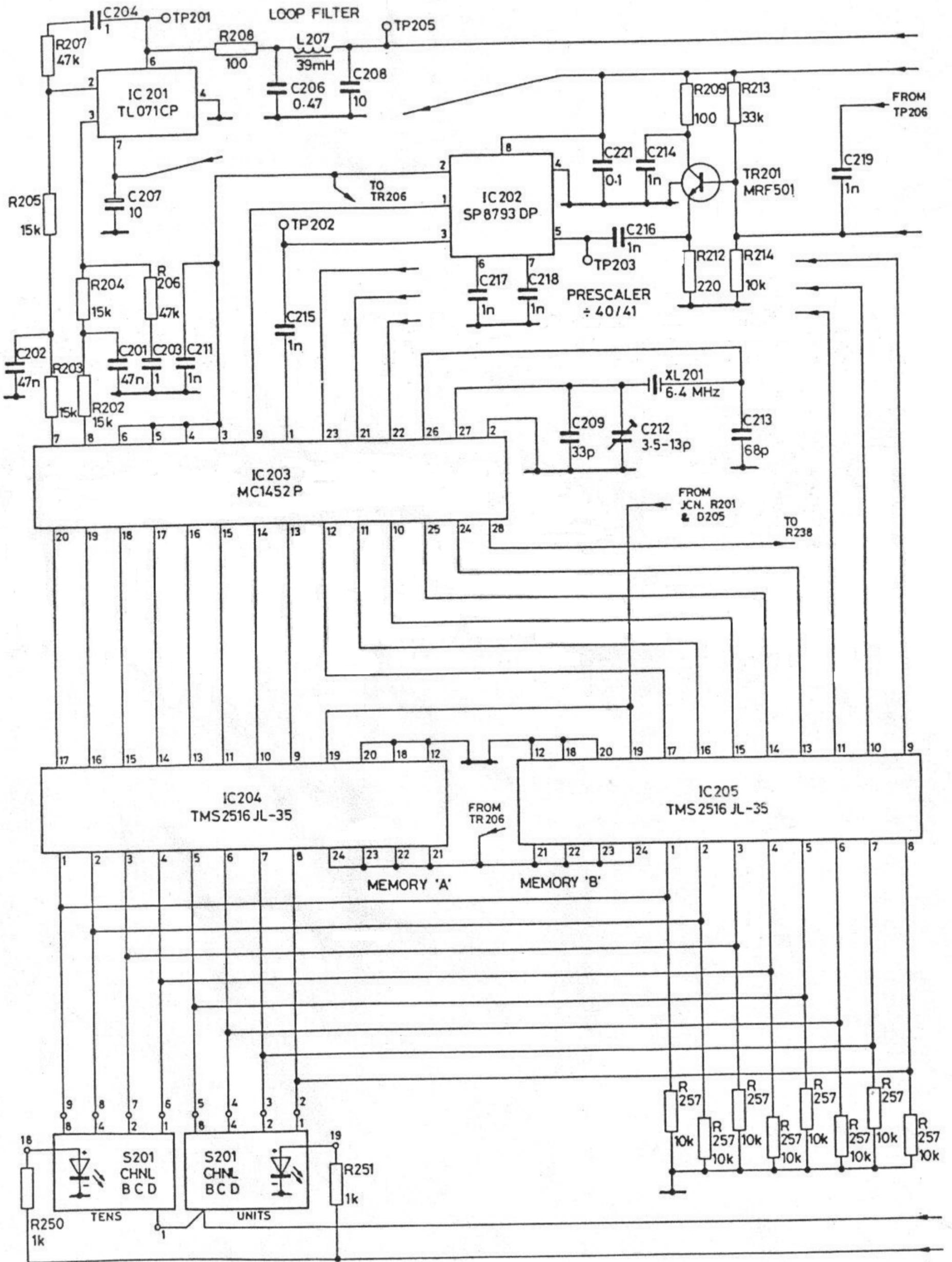


NOTE: - COMPONENTS MARKED THUS \* LISTED IN TABLE ON SHT. 2.

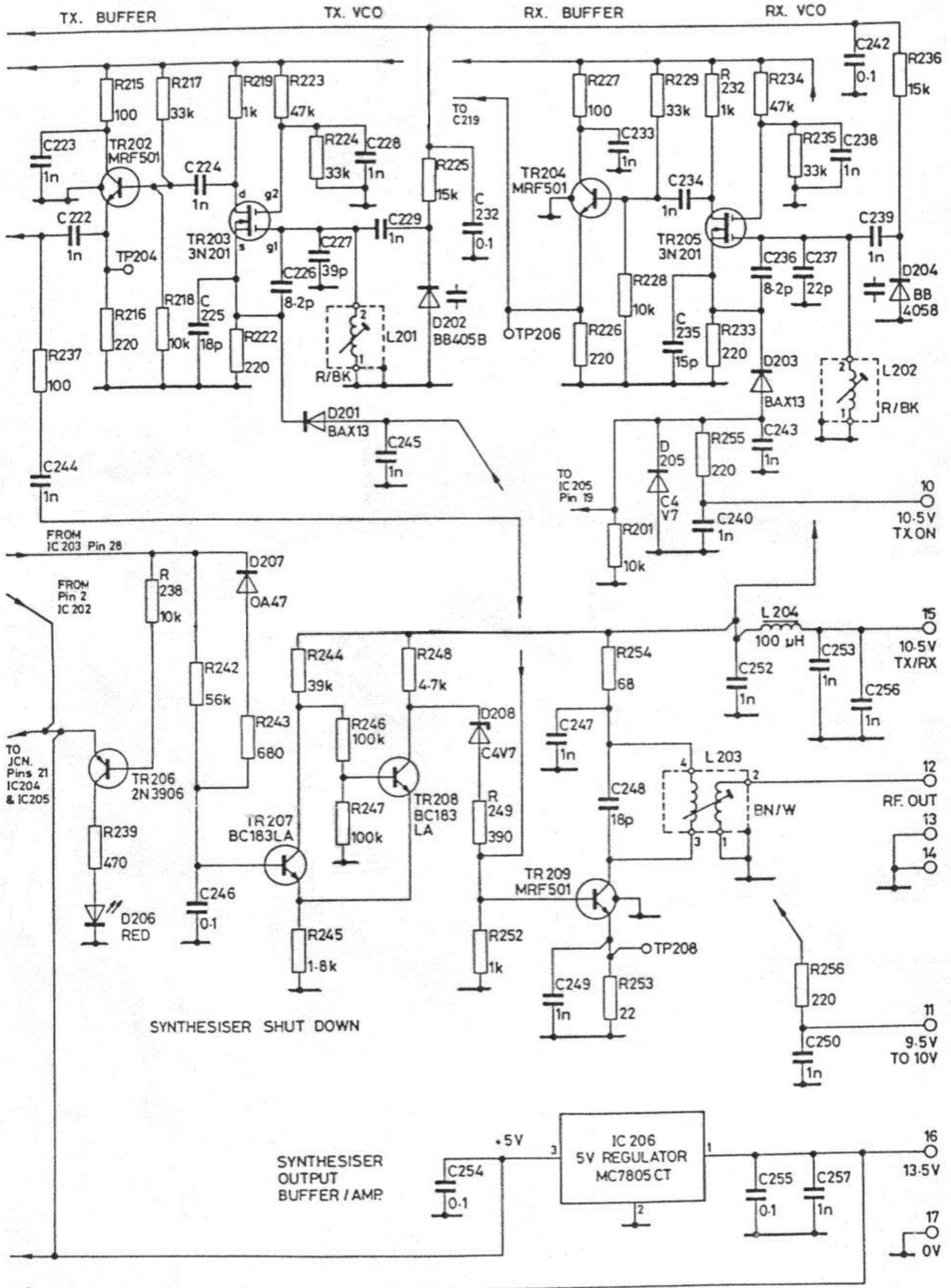
RECEIVER CIRCUIT DIAGRAM Fig 18



RECEIVER CIRCUIT DIAGRAM Fig 18



SYNTHESISER CIRCUIT DIAGRAM Fig 22



SYNTHESISER CIRCUIT DIAGRAM Fig 22